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Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of
Inventors: Keiichiro Kata and Shinichi Chikaki
For: PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR
WAFER

Attorney's Docket: WN-1439

Enclosed are:

X 4 sheets of ☐ informal ☒ formal drawings consisting of figures 1A - 4C.

X An assignment of the invention to: NEC Corporation.

X A certified copy of Japanese application Serial No. 6-237653.

☒ An information disclosure statement with Form PTO-1449 and referenced art (4 refs.).

A verified statement to establish small entity status under 37 C.F.R. §§ 1.9 and 1.27.

X A combined declaration and power of attorney.

A preliminary amendment.

A Notice Of Change Of Firm Name And Address.

The filing fee has been calculated as shown below:

FOR: No. Filed No. Extra

BASIC FEE

Total Claims 11 -20 = 0

Indep Claims **5 - 3 = 2**

Multiple Depend Claims

Small Entity	
Rate	Fee
	\$365.00
× \$ 11	\$
× \$ 38	\$
× \$120	\$
Total =	\$
Assign =	\$

Other Than a Small Entity	
Rate	Fee
× \$ 22	\$730.00
× \$ 76	\$
× \$240	\$152.00
	\$
Total =	\$882.00
Assign =	\$ 40.00

A check is attached in the amount of \$922.00 to cover the ☒ filing fee and the ☒ recording fee.

Respectfully submitted,

Sean M. McGinn
Reg. No. 34,386